

09/29/03 16805 U.S. PTO

PATENT

Attorney Docket No. NETL.001DV2

Date: September 29, 2003

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Mail Stop PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Transmitted herewith for filing is the patent application of

Inventors: **Jayesh R. Bhakta and Robert S. Pauley, Jr.**

For: **ARRANGEMENT OF INTEGRATED CIRCUITS IN A MEMORY MODULE**

Enclosed are:

- (X) Specification in fourteen (14) pages.
- (X) Nine (9) sheets of drawings.
- (X) A check in the amount of \$750 to cover the filing fee is enclosed.
- (X) Return prepaid postcard.
- (X) This application is a divisional of prior application 10/094,512 filed March 7, 2002.
- (X) Incorporation by Reference. The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.

CLAIMS AS FILED

FOR	NUMBER FILED	NUMBER EXTRA	RATE	FEE
Basic Fee			\$750	\$750
Total Claims	9 - 20 =	0 ×	\$18	\$0
Independent Claims	1 - 3 =	0 ×	\$84	\$0
If application contains any multiple dependent claims(s), then add			\$280	\$0
TOTAL FILING FEE		\$750		

- (X) The Commissioner is hereby authorized to charge any additional fees which may be required, now or in the future, or credit any overpayment to Account No. 11-1410.

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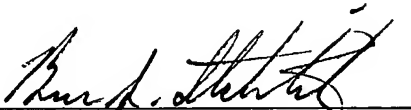
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(X) Please use Customer No. **20,995** for the correspondence address.



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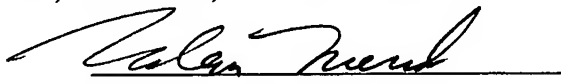
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Attorney Docket No. : NETL.001DV2
Applicant(s) : Jayesh R. Bhakta and Robert S. Pauley, Jr.
For : ARRANGEMENT OF INTEGRATED
CIRCUITS IN A MEMORY MODULE
Attorney : Bruce S. Itchkawitz
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I hereby certify that the accompanying

Transmittal letter; specification in 14 pages; 9 sheets of drawings; a copy of
Declaration by Inventors in one page from prior application; Check for Filing Fee;
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are being deposited with the United States Postal Service "Express Mail Post Office to
Addressee" service under 37 CFR 1.10 on the date indicated above and are addressed to the
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